

0002-28.vsd1

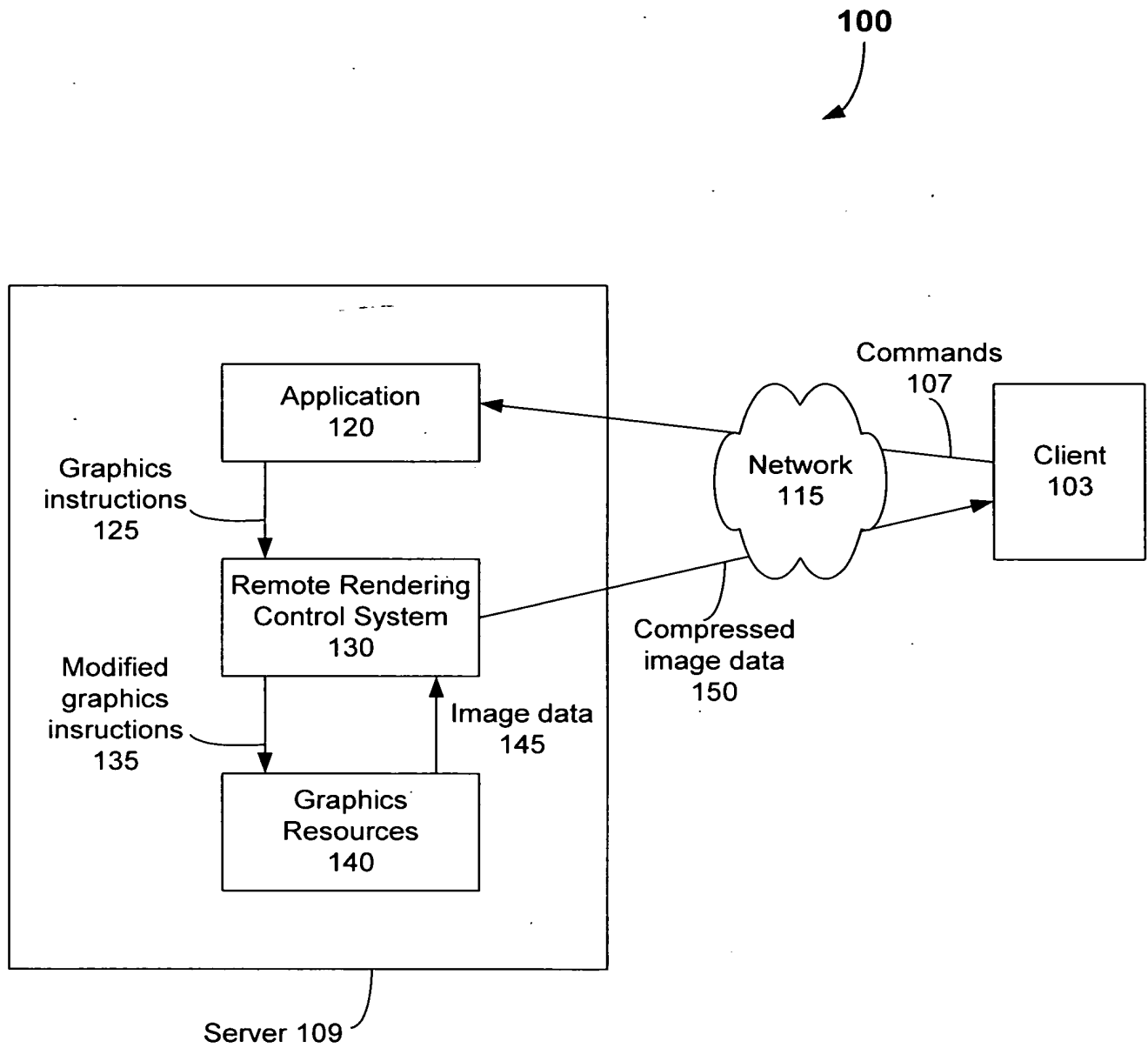


FIG. 1

0002-28.vsd2

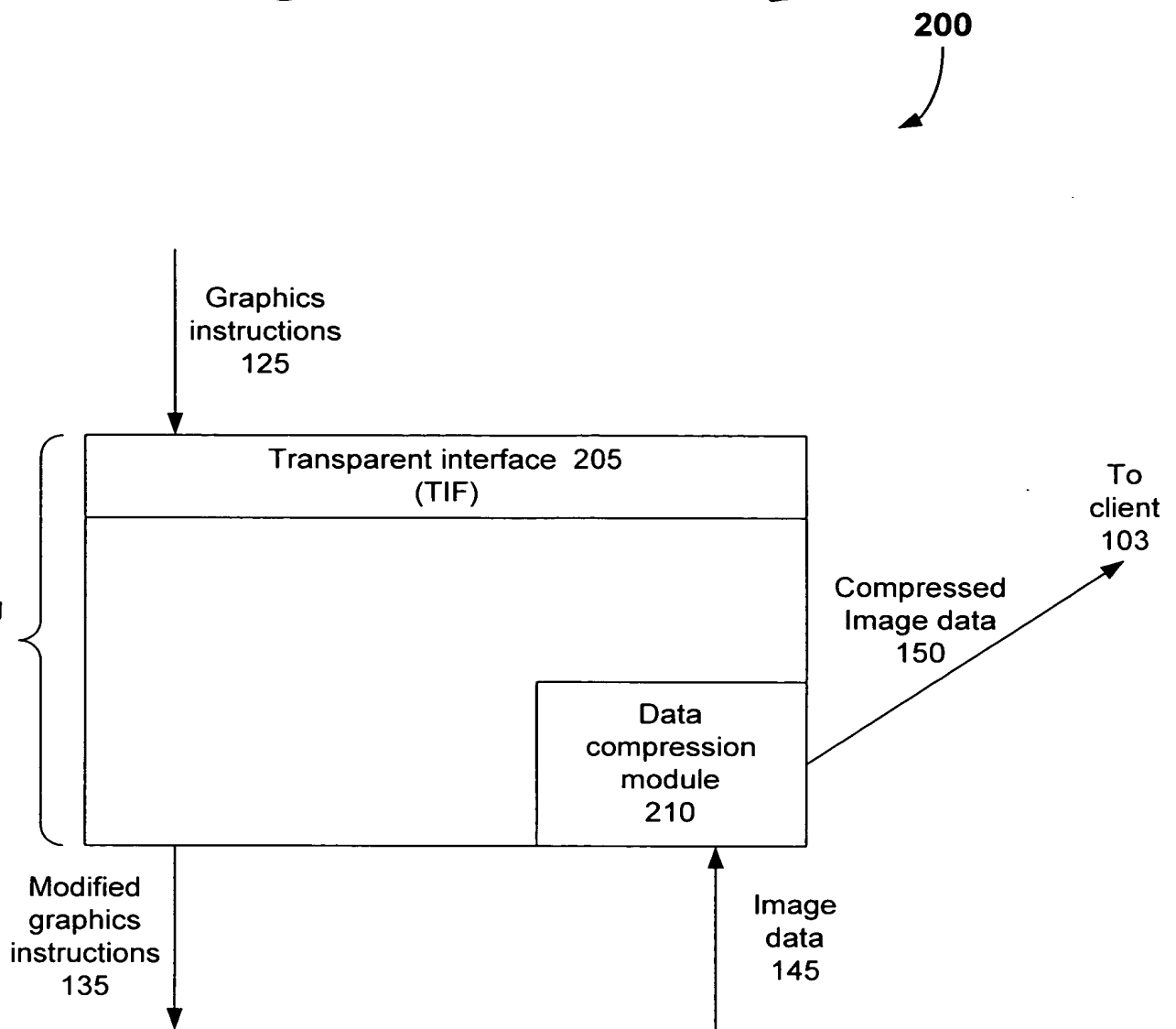


FIG. 2

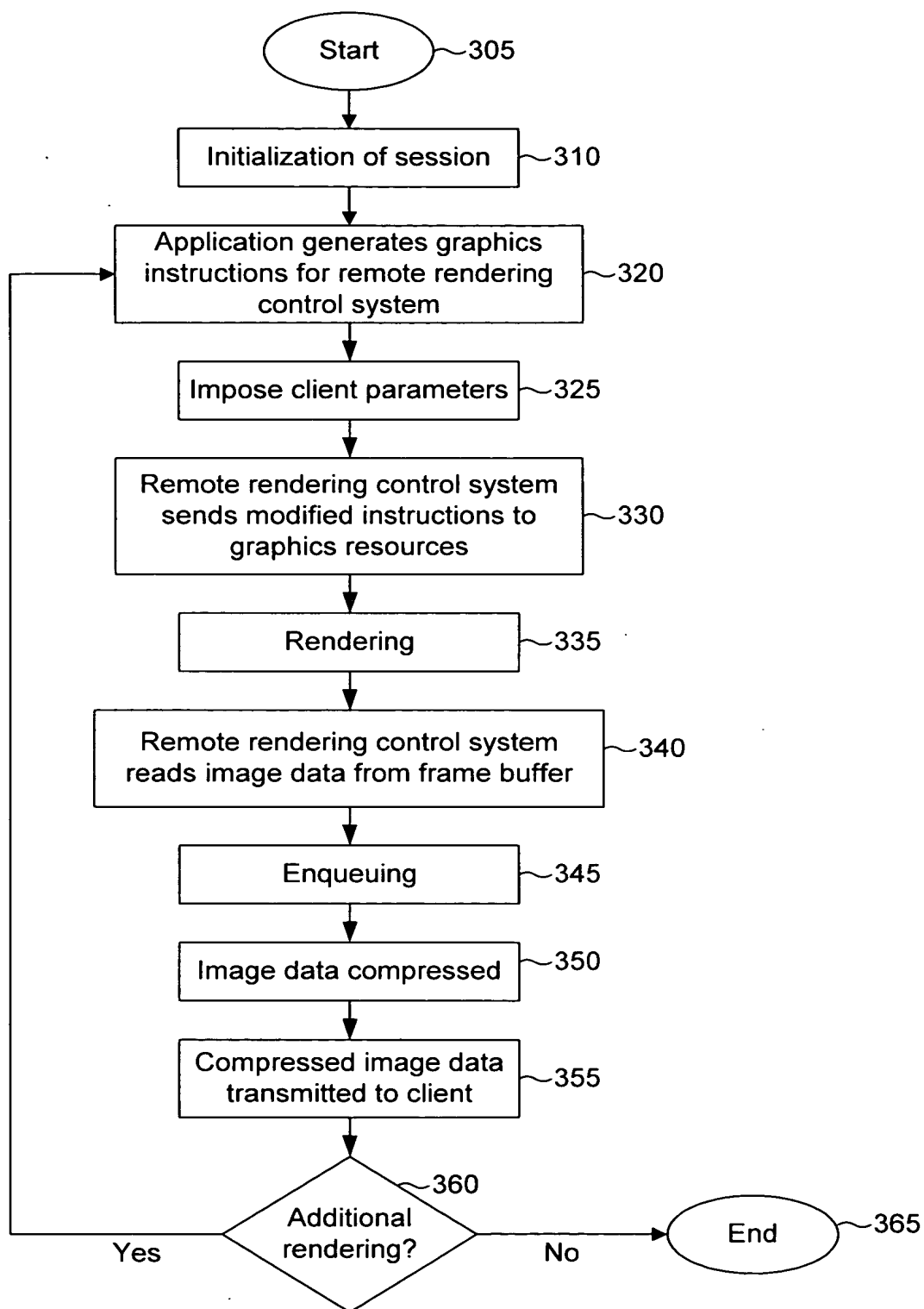
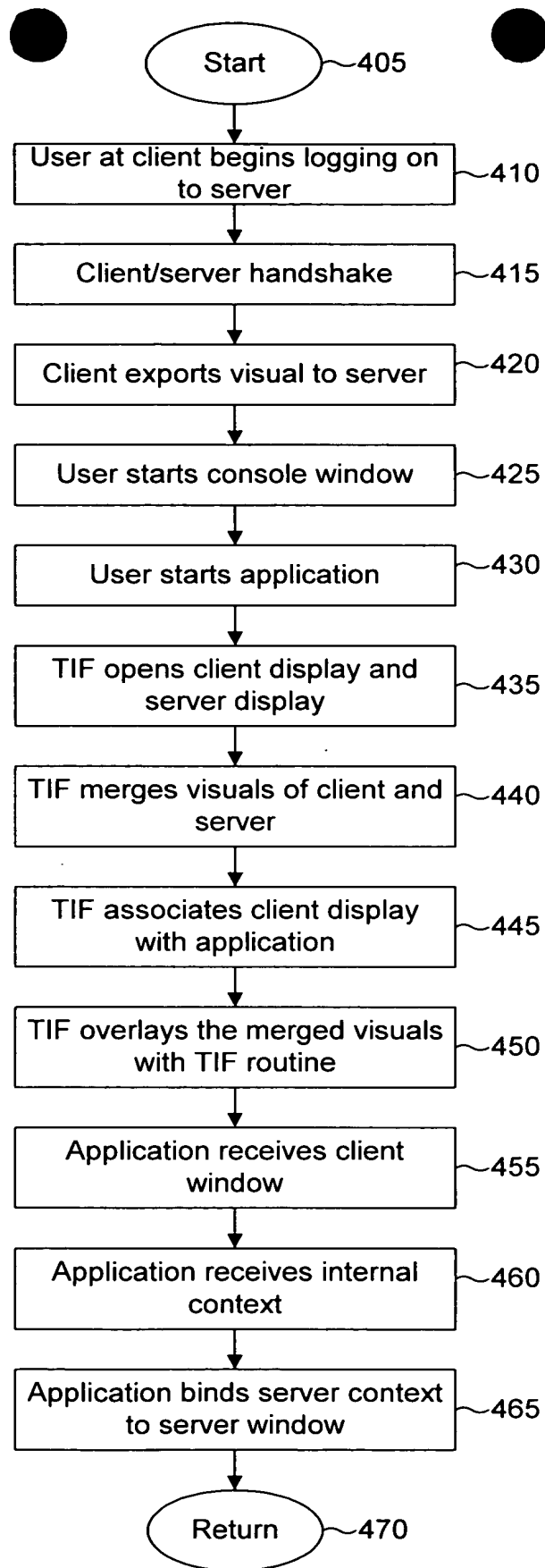


FIG. 3



310

FIG. 4

0002-28.vsd4

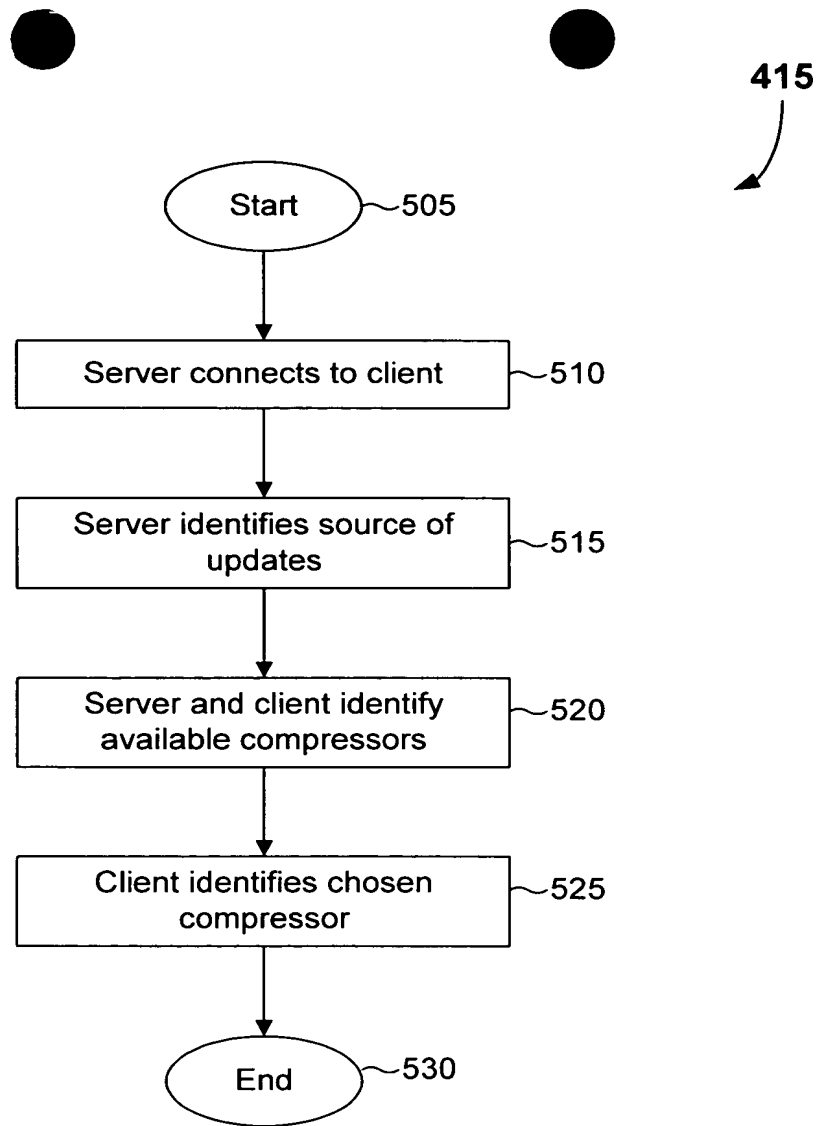


FIG. 5

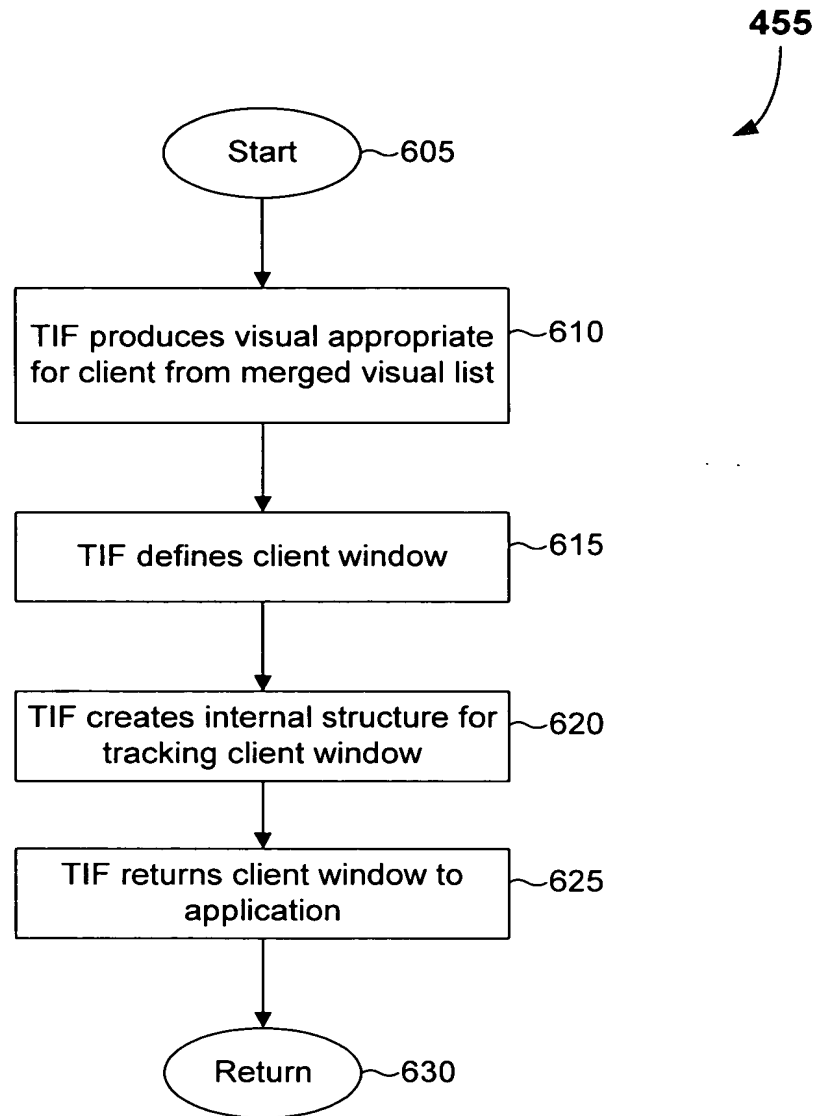


FIG. 6

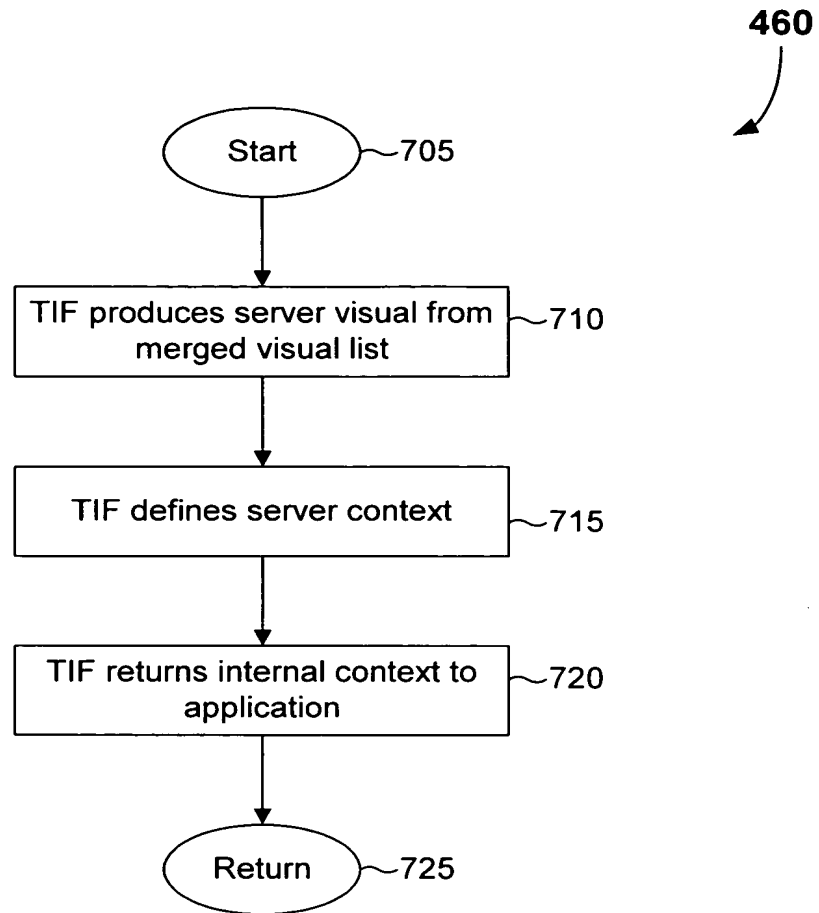


FIG. 7

09629458.073100

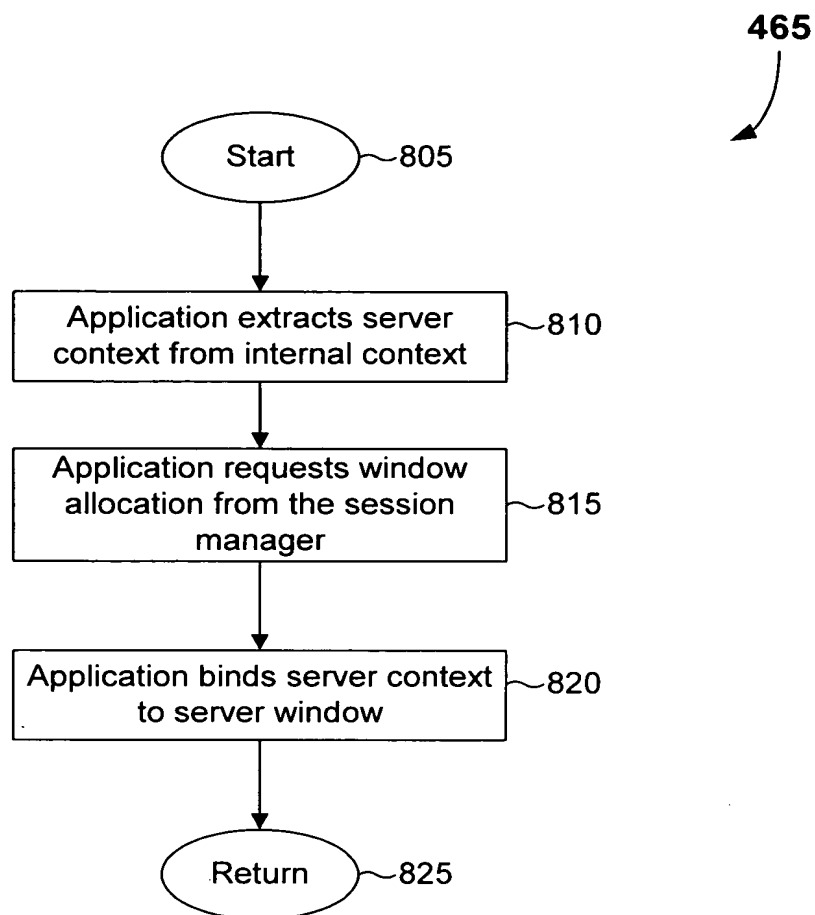


FIG. 8

0002-28.vsd9

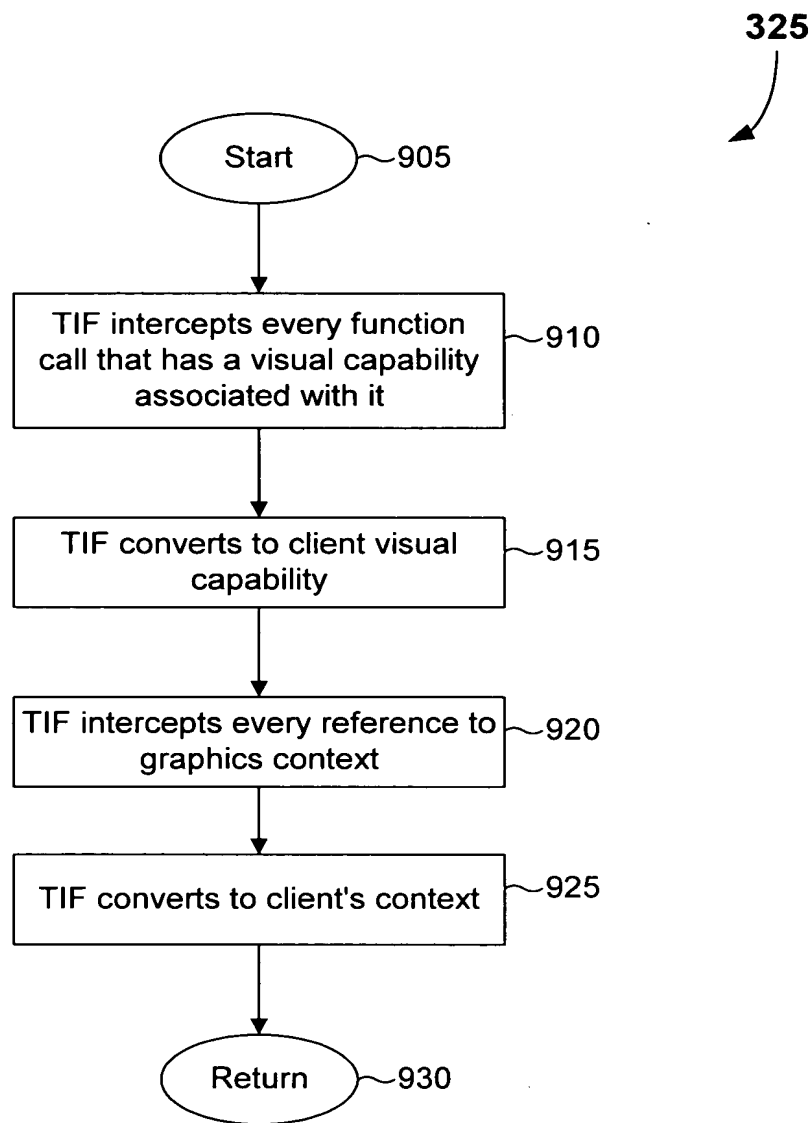


FIG. 9

09622458.073100
00TE20"85462960

Time
1010
↓

t_0

t_1

t_2

	Render	Compress	Transmit
t_0	A		
t_1	B	A	
t_2	C	B	A

Processes 1005 →

1000
↙

FIG. 10

1100

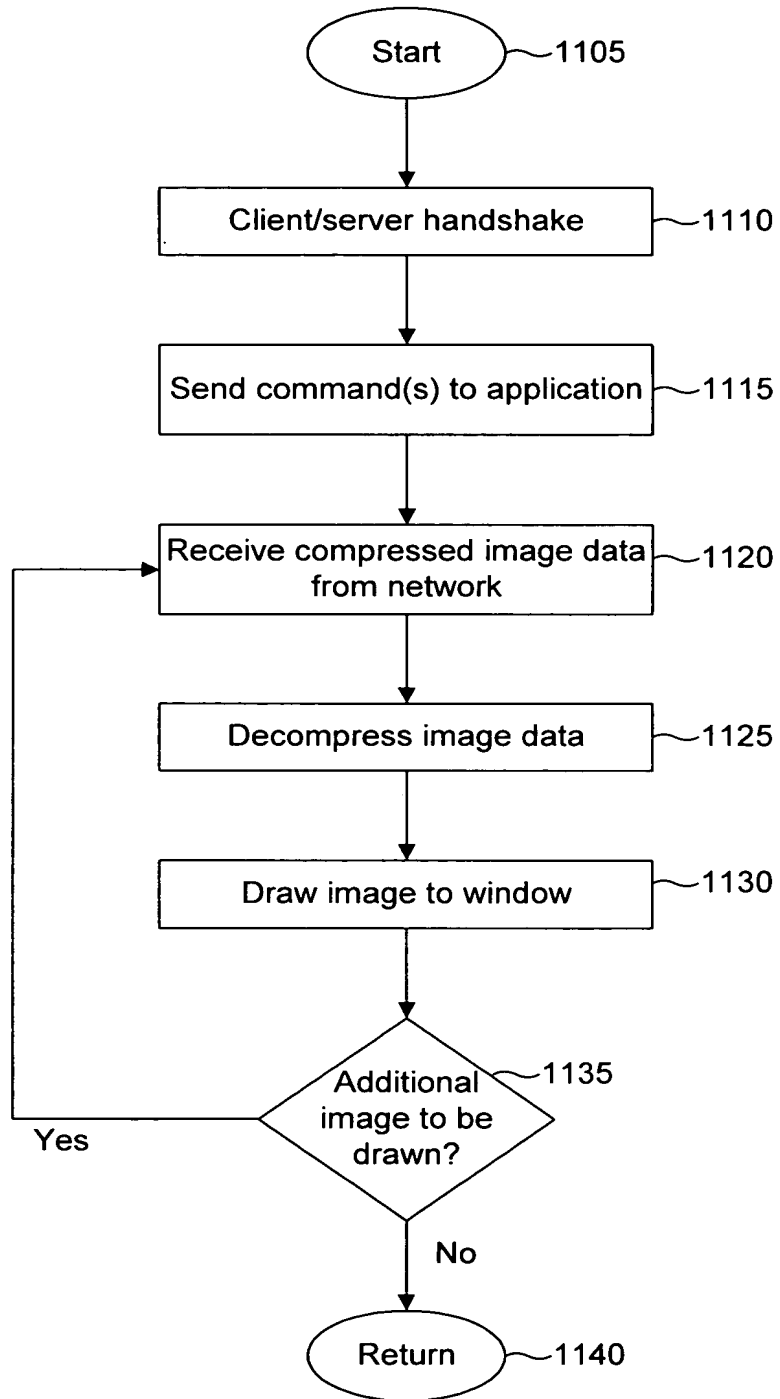


FIG. 11

Computer System 1200

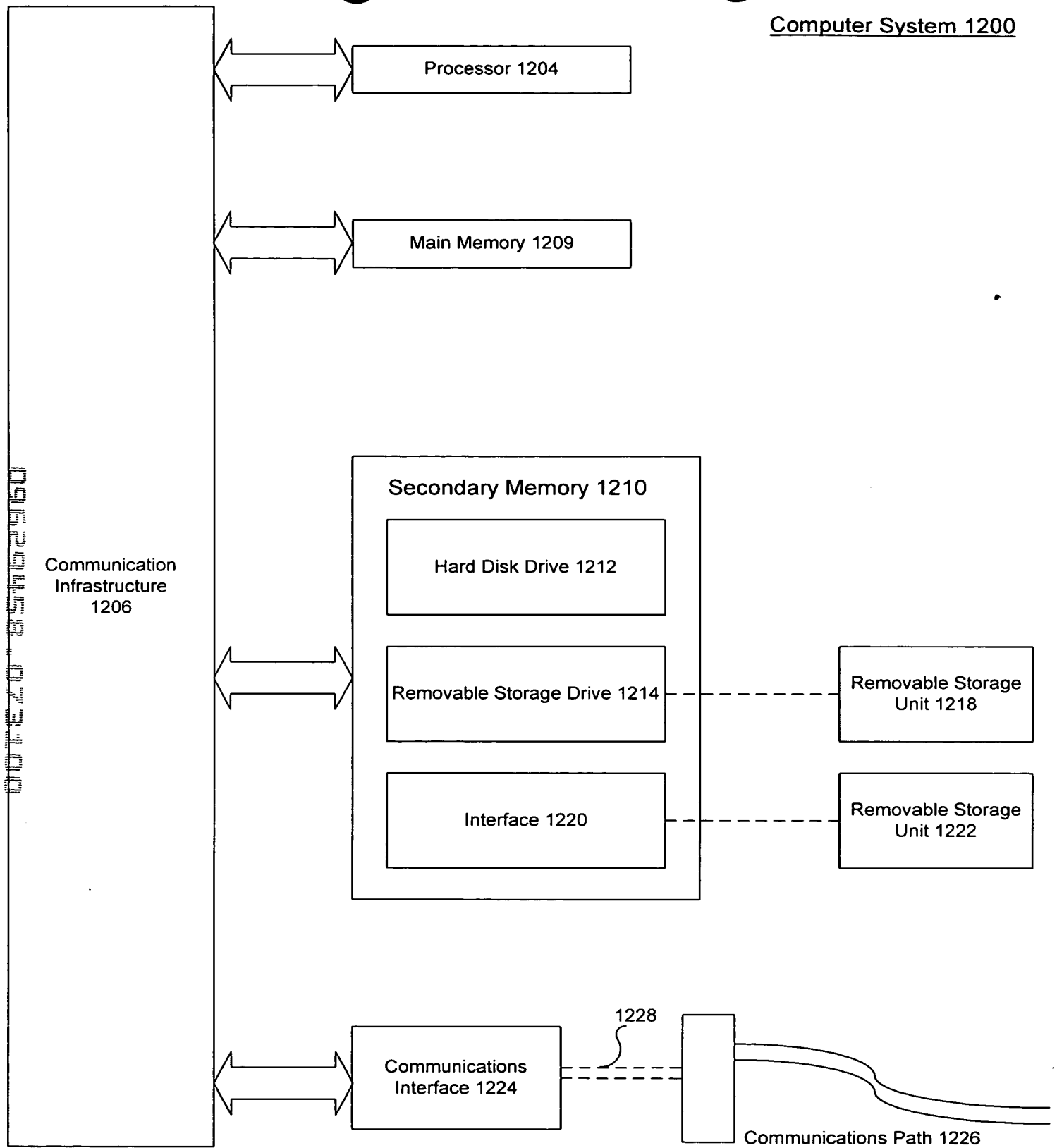


FIG. 12